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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/071,453
Filing Date February 8, 2002
Inventor Luan C. Tran
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner L.M. Schillinger
Attorney's Docket No. MI22-1921
Title: Semiconductor Processing Methods of Forming Transistors, Semiconductor
Processing Methods of Forming Dynamic Random Access Memory Circuitry,
and Related Integrated Circuitry

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT


References –See Attached Form PTO-1449

The Examiner's attention is directed to the references which are listed
on the attached Form PTO-1449, copies of which are attached. No admission
is made regarding whether all the submitted references are prior art.

Citation of the referenced art is respectfully requested.

Respectfully submitted,

Dated: 4-1-04

By: 
D. Brent Kenady
Reg. No. 40,045

